

Application No.: 10/708,297

Docket No.: JCLA12040

AMENDMENTSIn The Claims:

1. (original) A multi-domain vertical alignment thin film transistor liquid crystal display (MVA TFT-LCD), comprising:
  - a multi-domain vertical alignment thin film transistor liquid crystal display panel;
  - a first wide viewing film on a first surface of the thin film transistor liquid crystal display panel;
  - a first polarizer film on the first viewing film, wherein a surface of the first polarize film has a diffusive pattern;
  - a second wide viewing film on a second surface of the thin film transistor liquid crystal display panel; and
  - a second polarizer film on the second wide viewing film.
2. (original) The multi-domain vertical alignment thin film transistor liquid crystal display of claim 1, wherein the surface of the first polarizer film has a patterned surface as to form the diffusive pattern.
3. (withdrawn) The multi-domain vertical alignment thin film transistor liquid crystal display of claim 1, wherein a diffusive film is formed on the surface of the first polarizer film as to form the diffusive pattern.
4. (original) A multi-domain vertical alignment thin film transistor liquid crystal display (MVA TFT-LCD), comprising:

Application No.: 10/708,297

**Docket No.: JCLA12040**

- a thin film transistor array substrate;
- a color filter substrate over the thin film transistor array substrate, wherein the color filter substrate has diffusive particles;
- a liquid crystal layer between the thin film transistor array substrate and the color filter substrate;
- a first wide viewing film on a surface of the color filter substrate;
- a first polarizer on the first wide viewing film;
- a second wide viewing film on a surface of the thin film transistor array substrate; and
- a second polarizer on the second wide viewing film.

5. (original) The multi-domain vertical alignment thin film transistor liquid crystal display of claim 4, wherein a color photoresist layer of the color filter substrate has the diffusive particles.

6. (withdrawn) The multi-domain vertical alignment thin film transistor liquid crystal display of claim 4, wherein a diffusive film is formed on a color photoresist layer of the color filter substrate.

7. (withdrawn) A method of fabricating a polarizer film applied to a multi-domain vertical alignment thin film transistor liquid crystal display, the method comprising:

forming an optical thin film on a substrate; and  
forming a diffusive pattern on a surface of the optical thin film for forming a polarizer film  
having a diffusive pattern.

Application No.: 10/708,297

Docket No.: JCLA12040

8. (withdrawn) The method of fabricating a polarizer film of claim 7, wherein the step of forming the diffusive pattern on the surface of the optical thin film comprises photolithographic and etching technologies for forming the diffusive pattern on the surface of the optical thin film.

9. (withdrawn) The method of fabricating a polarizer film of claim 7, wherein the step of forming the diffusive pattern on the surface of the optical thin film comprises forming a diffusive film on the surface of the optical thin film.

10. (withdrawn) A method of fabricating a color filter substrate applied to a multi-domain vertical alignment thin film transistor liquid crystal display, the method comprising:

forming a black matrix and a color photoresist layer on a substrate, wherein the color photoresist layer has diffusive particles.

11. (withdrawn) The method of fabricating a color filter substrate of claim 10, wherein the step of forming the color photoresist layer having the diffusive particles comprises forming a diffusive film on a surface of the color photoresist layer and the diffusive film is a transparent film having the diffusive particles.

12. (withdrawn) The method of fabricating a color filter substrate of claim 10, wherein the step of forming the color photoresist layer having the diffusive particles comprises spreading the diffusive particles therein.